


## DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Name	<b>Giri S. Kulkarni</b>	
Date of Birth	09/01/1968	
Highest Qualification	M.Tech. (IIT Bombay)	
Branch / Specialization	Micro Electronics & VLSI	
Designation	Associate Professor & HOD	
Department	Electronics & Telecommunication	
Date of Joining the College	14/07/1997	
Previous Experience	<ol style="list-style-type: none"> <li>1) Teaching – 04 Years</li> <li>2) Industry -- 01 Year</li> </ol>	
Number of Papers, Reports, Reviews, Books Presented / Published	01	
Details	<p>“Highly Robust Nanoscale Planar Double-Gate MOSFET Device and SRAM Cell Immune to Gate-Misalignment and Process Variations”, Proceedings of the IEEE International Workshop on Electron Devices &amp; Semiconductor Technology, June 1-2, 2009, Mumbai, India</p>	
Number of Summer / Winter Schools, STTPs, Refresher Courses Attended	06	
Details	<ol style="list-style-type: none"> <li>1) IEEE International Workshop on Electron Devices &amp; Semiconductor Technology, June 1-2, 2009, Mumbai, India</li> <li>2) International Workshop on Physics of Semiconductor Devices from 16/12/2007 to 20/12/2007 at TIFR, Mumbai &amp; IIT Bombay.</li> <li>3) Certificate Course: Advanced Programming from 02/06/2007 to 01/07/2008 at IIT, Bombay.</li> <li>4) Workshop: VLSI Design Technologies from 12/08/2004 to 14/08/2004 at SHAH &amp; Anchor COE, Mumbai</li> <li>5) Workshop: Advances in Microelectronics on 15/03/2003 at FAMT, Ratnagiri</li> <li>6) Workshop: Design of VLSI Systems &amp; Applications at ISS Hyderabad from 16/02/2001 to 18/02/2001</li> </ol>	
Details of student projects	Over 20 projects in the field of Microprocessor, Microcontroller and VLSI	

Membership in Professional Bodies	Life Member of Indian Society of Technical Education (ISTE) (LMISTE)
Awards / Honours / Prizes	Nil
Research / Sponsored / Consultancy Projects	Nil